HSK120

Silicon Epitaxial Planar Diode for High Speed Switching

HITACHI

ADE-208-171C(Z)

Rev. 3 Jan. 1999

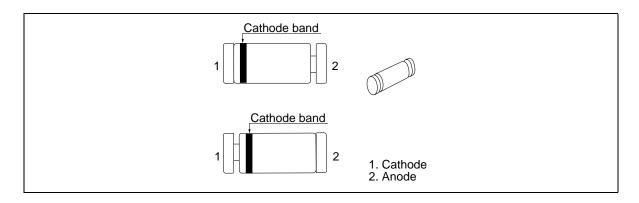
Features

- Low reverse recovery time. $(t_{rr} = 3.0 \text{ns max})$
- LLD package is suitable for high density surface mounting and high speed assembly

Ordering Information

Type No.	Cathode band	Package Code	
HSK120	White	LLD	

Outline





HSK120

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	$V_{_{RM}}$	70	V
Reverse voltage	V_R	60	V
Peak forward current	I _{FM}	450	mA
Non-Repetitive peak forward surge current	I _{FSM} * ¹	4	А
Average rectified current	Io	150	mA
Junction temperature	Tj	175	°C
Storage temperature	Tstg	-65 to +175	°C

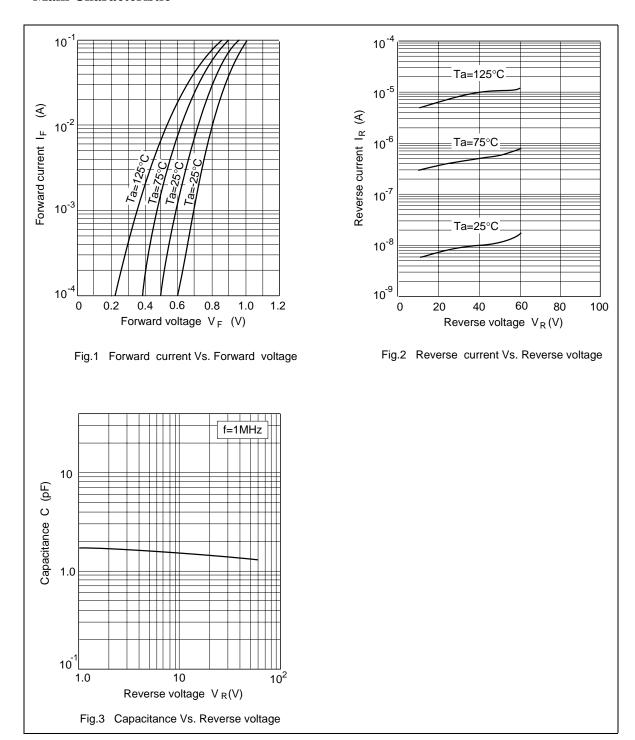
Note 1. Within 1µs forward surge current..

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

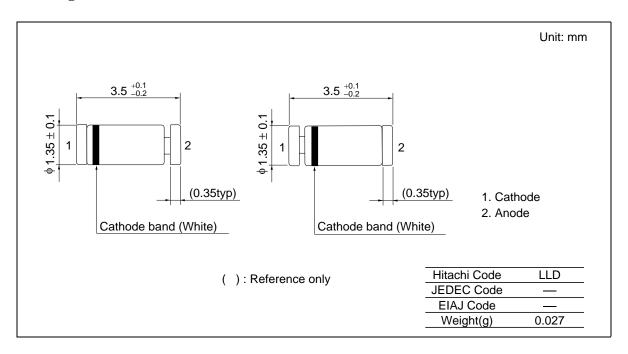
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	$V_{_{\rm F}}$	_	_	0.8	V	I _F = 10 mA
Reverse voltage	V_R	70	_	_	V	$I_R = 5\mu A$
Reverse current	I _R	_	_	0.1	μΑ	$V_R = 60V$
Capacitance	С	_	_	3.0	рF	$V_R = 0V, f = 1 MHz$
Reverse recovery time	t _{rr}	_		3.0	ns	$I_F = 10 \text{ mA}, V_R = 6V, R_L = 50\Omega, I_{rr} = 0.1I_R$

Main Characteristic



HSK120

Package Dimensions



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